

IN THE CLAIMS:

1. (previously presented) A method for storage and retrieval of programs and data within a PLC system, the PLC system including a plurality of modules including a memory host module including a CPU and memory, at least one option module including a CPU and memory, the at least one option module including a first option module and a second option module, the first module including a memory, a backplane interconnecting the memory host module and the at least one option module, the memory host module coupled to an external device, said method comprising the steps of:

storing a plurality of operating programs and data including a first and a second operating program and data in the external device coupled to the memory host module, wherein the first operating program and data corresponds to the first option module and the second operating program and data corresponds to the second option module;

retrieving the first operating program and data;

retaining the second operating program and data; and

transmitting the first operating program and data to the first option module.

2. (previously presented) A method according to Claim 1 further comprising the step of transferring the first operating program and data for the first option module from the memory of the memory host module to the memory of the first option module via the backplane.

3. (previously presented) A method according to Claim 1 further comprising the step of transferring the first operating program and data for the first option module from the memory of the first option module to the memory host module via the backplane.

4. (previously presented) A method according to Claim 1 wherein the memory host module is further configured with an external device interface, said method further comprising the step of transferring the first operating program and data for the first option module from the external device through the memory host module, to the memory of the first option module via the backplane.

5. (previously presented) A method according to Claim 1 wherein the memory host module is further configured with an external device interface, said method further comprising the step of transferring the first operating program and data for the first option module from the external device to the memory of the memory host module via the external device interface.

6. (previously presented) A method according to Claim 1 wherein the memory host module further configured with an interface to the external device, said method further comprising the step of transferring the first operating program and data for the first option module from the first option module through the memory host module via the backplane, to the external device.

7. (previously presented) A method according to Claim 1 wherein the memory host module further configured with an external device interface, said method further comprising the step of transferring the first operating program and data for the first option module from the memory of the memory host module to the external device via the external device interface.

8. (previously presented) A method according to Claim 1 wherein the memory host module further configured with an external device interface, the first option module further configured with an external device interface, said method further comprising the step of transferring the first operating program and data for the first option module from the memory of the memory host module to the memory of the first option module via the external device interfaces.

9. (previously presented) A method according to Claim 1, wherein the memory host module further configured with an external device interface, the first option module further configured with an external device interface, said method further comprising the step of transferring the first operating program and data for the first option module from the memory of the first option module to the memory of the memory host module via the external device interfaces.

10. ^{10.7} (previously presented) A memory host for a programmable logic controller (PLC) system, the system comprising at least one option module further comprising an option module memory, said at least one option module including a first option module and a second option module, said first option module including a memory, said memory host comprising a

memory, a central processing unit (CPU), and a backplane interface, said memory host coupled to an external device and configured to:

retrieve a first operating program and data, wherein the first operating program and data corresponds to said first module;

retain a second operating program and data, wherein the second operating program and data corresponds to said second module; and

transmit the first operating program and data to the first option module, wherein said external device is configured to store a plurality of operating programs and data including the first operating program and data and the second operating program and data.

11. ¹³ (previously presented) A memory host in accordance with Claim ¹²10 further configured to selectively transfer the first operating program and data from said memory host to said memory of said first option module via said backplane interface.

12. ¹⁴ (previously presented) A memory host in accordance with Claim ¹²10 further configured to automatically transfer the first operating program and data from said memory host to said memory of said first option module via said backplane interface.

13. ¹⁵ (previously presented) A memory host in accordance with Claim ¹²10 further configured with means to transfer the first operating program and data from said memory host to said memory of said first option module via said backplane interface.

14. ¹⁶ (previously presented) A memory host in accordance with Claim ¹²10 further configured to retrieve the first operating program and data from said memory of said first option module via said backplane interface for storage.

15. ¹⁷ (previously presented) A memory host in accordance with Claim ¹²10 further configured with means to retrieve the first operating program and data from the memory of said first option module via said backplane interface for storage.

16. ¹⁸ (previously presented) A memory host in accordance with Claim ¹²10 further comprising an external device interface adapted to be connected to said external device, and further configured to transfer the first operating program and data from the external device to said memory host via said external device interface.

~~17.~~¹⁷ (original) A memory host in accordance with Claim ~~16~~¹⁸ wherein said external device interface is a serial interface.

~~18.~~²⁰ (previously presented) A memory host in accordance with Claim ~~16~~¹⁸ further configured to transfer the first operating program and data from the external device via the external device interface to the memory of said first option module via said backplane interface.

~~19.~~ (canceled)

~~20.~~²¹ (previously presented) A memory host in accordance with Claim ~~16~~¹⁸ further configured to transfer the first operating program and data from the memory of said first option module via said backplane interface to the external device via the external device interface.

~~21.~~²² (previously presented) A memory host in accordance with Claim ~~16~~¹⁸ further configured to transfer the first operating program and data from said memory host to the external device via the external device interface.

~~22.~~²³ (previously presented) A memory host in accordance with Claim ~~16~~¹⁸ further configured to transfer the second operating program and data from the memory host via the external device interface to said second option module.

~~23.~~²⁴ (currently amended) A memory host in accordance with Claim ~~16~~¹⁸ further configured to transfer the second operating program and data from said second option module that further comprises an external device interface to said memory of said memory host via the external device interfaces.

~~24.~~²⁵ (previously presented) A memory host in accordance with Claim ~~16~~¹² wherein said memory of the memory host comprises flash memory.

~~25-34.~~ (canceled)

~~35.~~¹⁰ (previously presented) A method according to Claim 1 further comprising controlling said PLC system and other PLC systems via said external device.

~~36.~~²⁶ (previously presented) A memory host in accordance with Claim ~~10~~¹² wherein said external device is coupled to said memory host via Internet.

27. (new) A method in accordance with Claim 1 further comprising coupling said memory host module to at least two of the at least one option module.

38. (new) A memory host in accordance with Claim 10 wherein said memory host is coupled to at least two of the at least one option module.
